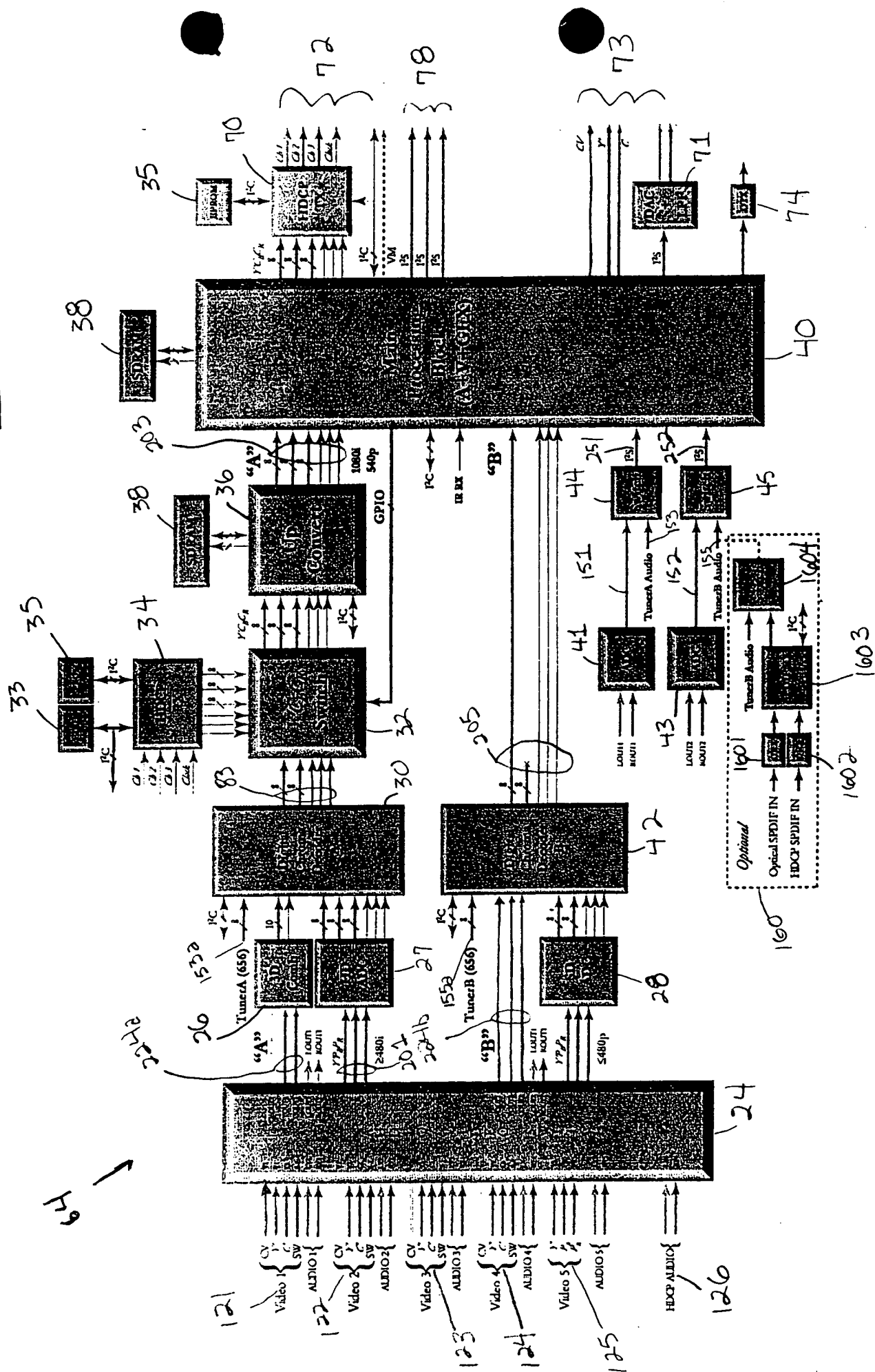


FIG. 1



25.2

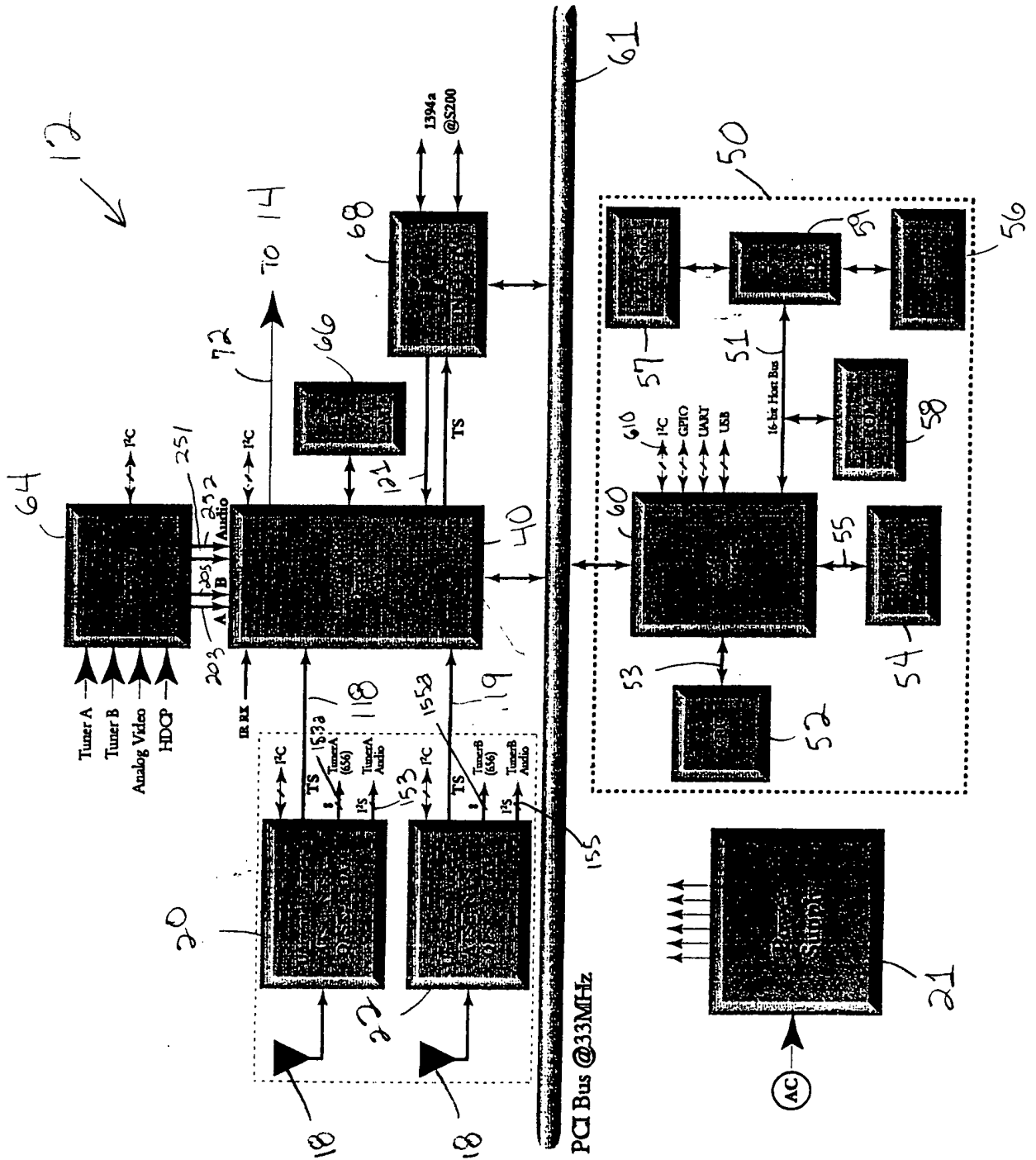


FIG. 3

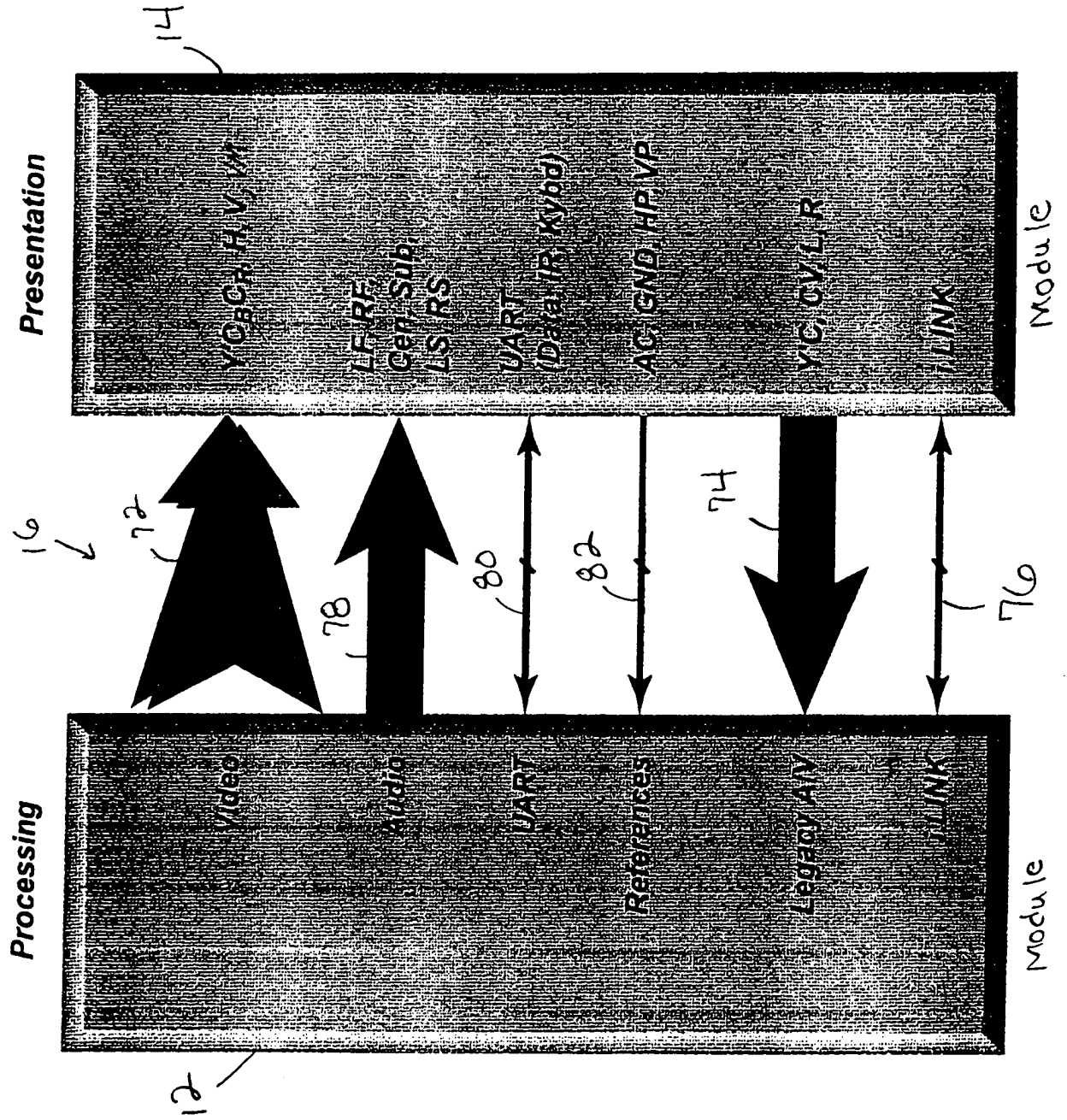


FIG. 4

The diagram illustrates a video monitor system (14) with the following components and signal paths:

- Input (72):** Receives video signals (A1, A2, A3, A4) and control signals (PC, OSD, KYBD).
- Block 80:** Processes the input signals, outputting OSD, PC, and KYBD signals.
- Block 84:** Receives AC power and outputs a signal to block 90.
- Block 86:** Receives a signal from block 80 and outputs a signal to block 90.
- Block 90:** Receives signals from blocks 84 and 86, and outputs a signal to block 91.
- Block 91:** Receives a signal from block 90 and outputs a signal to block 92.
- Block 92:** Receives a signal from block 91 and outputs a signal to block 93.
- Block 93:** Receives a signal from block 92 and outputs a signal to block 94.
- Block 94:** Receives a signal from block 93 and outputs a signal to block 95.
- Block 95:** Receives a signal from block 94 and outputs a signal to block 96.
- Block 96:** Receives a signal from block 95 and outputs a signal to block 98.
- Block 98:** Receives a signal from block 96 and outputs a signal to block 100.
- Block 100:** The final output of the system, which is a video signal.

Additional labels and specifications include:

- 31.5Hz Anode:** A label for the output signal from block 98.
- 60Hz:** A frequency specification for the signal path between blocks 93 and 94.
- 31.5Hz:** A frequency specification for the signal path between blocks 94 and 95.
- AC:** An alternating current power source connected to block 84.
- OSD, PC, KYBD:** Control signals originating from the input (72) and block 80.
- A1, A2, A3, A4:** Input video signals.

515